Electronic Acknowledgement Receipt				
EFS ID:	7647601			
Application Number:	10551324			
International Application Number:				
Confirmation Number:	7460			
Title of Invention:	SEMICONDUCTOR TRANSISTOR (DMOS) DEVICE FOR USE AS A POWER AMPLIFIER			
First Named Inventor/Applicant Name:	Radjindrepersad Gajadharsing			
Customer Number:	20995			
Filer:	Eli A. Loots/Chelsea Pearsall			
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Application Type:	U.S. National Stage under 35 USC 371			

Payment information:

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for PTA recalculation in view of Wyeth	_ ' _ C.pdf	43675	no	1
			ca9e3fc5b24cfb5ce8d9d1d228c34325120f 8097		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.